

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Shaw (et al.)**  
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Examiner: **Aimee J. Li**  
Group Art Unit: **2183**

Title: **RISC Microprocessor Architecture**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Customer No.: **40972**

**TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT**

Enclosed for filing in the above-referenced application are the following:

1. Information Disclosure Statement (citing 8 total references);
2. Form PTO-1449 (citing 8 total references); and
3. 8 non-United States cited references.

Respectfully Submitted,

August 8, 2008  
Date: \_\_\_\_\_

/Larry E. Henneman, Jr./

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**CERTIFICATE OF ELECTRONIC FILING**

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being filed with the United States Patent and Trademark Office on the date shown below via the Office's Electronic Filing System.

Date: August 8, 2008

/Larry E. Henneman, Jr./

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